



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814

Examiner: Anh D. Mai

Handwritten notes: A/D, OK, 9/29/02

In Re PATENT APPLICATION Of:

Applicant : Hiroki NAKAMURA)

Serial No. : 09/625,178)

Filed : July 25, 2000)

For : SEMICONDUCTOR DEVICE HAVING)
WIRING PATTERNS AND DUMMY)
PATTERNS COVERED WITH)
INSULATING LAYER (twice amended))

Attorney Ref. : F98ED0762)

AMENDMENT

RECEIVED
SEP 13 2002
TECHNOLOGY CENTER 2800

September 10, 2002

BOX: Non-Fee Amendment

Commissioner for Patents
Washington, D.C. 20231

Sir:

This Amendment is submitted with the applicant's Request for Continued Examination pursuant to 37 C.F.R. §1.114(a). Please amend the above-identified application as follows: